رم (۲<sup>۳</sup> 54LS/74LS249 V

## **BCD TO 7-SEGMENT DECODER**

(With Open-Collector Outputs)

A1 1 16 Vcc
A2 2 15 f
LT 3 14 g
BI/RBO 4 13 a
RBI 5 12 b
A3 6 11 c
A0 7 10 d
GND 8 9 e

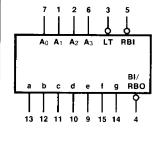
CONNECTION DIAGRAM PINOUT A

**DESCRIPTION** — The 'LS249 has active HIGH open-collector outputs and is the 16-pin version of the 14-pin 'LS49. The 'LS249 incorporates the Lamp Test and BI/RBO inputs that are omitted in the 'LS49. Additionally, the 'LS249 will light the top bar (segment a) for numeral 6 and the bottom bar (segment d) for numeral 9. For detailed description and specifications please refer to the 'LS49 data sheet.

**ORDERING CODE:** See Section 9

	PIN	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
PKGS	оит	$V_{CC} = +5.0 \text{ V } \pm 5\%,$ $T_A = 0^{\circ} \text{ C to } +70^{\circ} \text{ C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%,$ $T_A = -55^{\circ}\text{C to} + 125^{\circ}\text{C}$	
Plastic DIP (P)	Α	74LS249PC		9B
Ceramic DIP (D)	А	74LS249DC	54LS249DM	6B
Flatpak (F)	Α	74LS249FC	54LS249FM	4L

## LOGIC SYMBOL



V<sub>CC</sub> = Pin 16 GND = Pin 8

## INPTU LOADING/FAN-OUT: See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	<b>54/74LS (U.L.)</b> HIGH/LOW
A <sub>0</sub> — A <sub>3</sub>	BCD Inputs	0.5/0.25
A <sub>0</sub> — A <sub>3</sub> BI LT	Blanking Input (Active LOW)	0.5/0.25
<u>T</u> _	Lamp Test Input (Active LOW)	0.5/0.25
BI/RBO	Blanking Input (Active LOW) or	0.5/0.25
	Ripple Blanking Output (Active LOW)	1.25/2.0
		(1.0)
a — g	Segment Outputs (Active HIGH)	OC*/5.0
		(2.5)

\*OC - Open Collector